

R07DS0340EJ0100

Rev.1.00

Apr 18, 2011

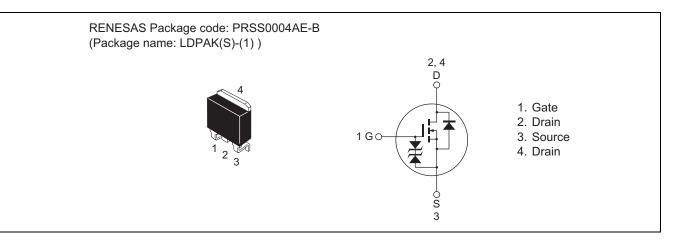
# RJK0630JPE

Silicon N Channel MOS FET High Speed Power Switching

### Features

- For Automotive application
- AEC-Q101 compliant
- Low on-resistance :  $R_{DS(on)} = 6.2 \text{ m}\Omega \text{ typ.}$
- Capable of 4.5 V gate drive
- Low input capacitance : Ciss = 2100 pF typ.

#### Outline



## **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Value	Unit
Drain to source voltage	V <sub>DSS</sub>	60	V
Gate to source voltage	V <sub>GSS</sub>	±20	V
Drain current	ID	75	А
Drain peak current	I <sub>D</sub> (pulse) Note1	300	А
Body-drain diode reverse drain current	I <sub>DR</sub>	75	А
Body-drain diode reverse drain peak current	I <sub>DR</sub> (pulse) Note1	300	А
Avalanche current	AP Note2	35	А
Avalanche energy	E <sub>AR</sub> <sup>Note2</sup>	105	mJ
Channel dissipation	Pch Note3	85	W
Channel temperature	Tch Note4	175	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1.  $PW \leq 10~\mu s,\,duty~cycle \leq 1\%$ 

- 2. Tch = 25°C, Rg  $\geq$  50  $\Omega$
- 3. Tc = 25°C
- 4. AEC-Q101 compliant

#### **Thermal Impedance Characteristics**

• Channel to case thermal impedance  $\theta$ ch-c: 1.76°C/W



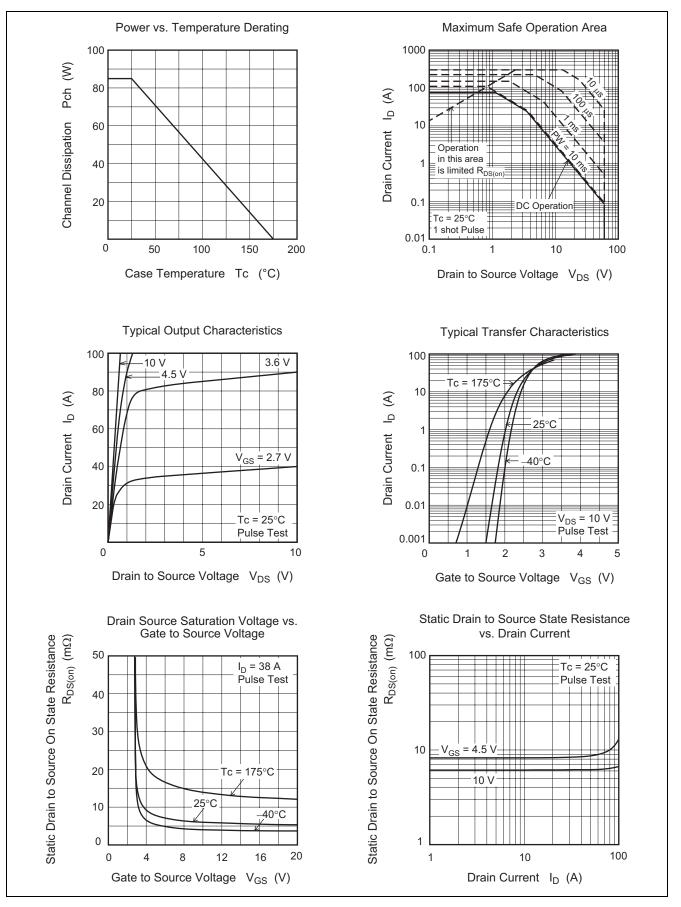
## **Electrical Characteristics**

						$(Ta = 25^{\circ}C)$
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Gate to source leak current	I <sub>GSS</sub>	—	—	±10	μA	$V_{GS}=\pm 20~V,~V_{DS}=0$
Zero gate voltage drain current	I <sub>DSS</sub>	—	—	1	μA	$V_{DS} = 60 V, V_{GS} = 0$
Gate to source cutoff voltage	V <sub>GS(off)</sub>	1.0	—	2.0	V	$I_D = 1 \text{ mA}, V_{DS} = 10 \text{ V}$
Static drain to source on state	R <sub>DS(on)</sub>	—	6.2	7.5	mΩ	$I_D = 38 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note5}}$
resistance		—	8.5	11.5	mΩ	$I_D = 38 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note5}}$
Input capacitance	Ciss	—	2100	_	pF	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 f = 1 MHz
Output capacitance	Coss	—	550	_	pF	
Reverse transfer capacitance	Crss	—	420	_	pF	
Total gate charge	Qg	—	49	_	nC	$V_{DD} = 10 \text{ V}, V_{GS} = 10 \text{ V},$ $I_D = 75 \text{ A}$
Gate to source charge	Qgs	—	7	_	nC	
Gate to drain charge	Qgd	—	15		nC	
Turn-on delay time	t <sub>d(on)</sub>	—	16	_	ns	$I_D$ = 38 A, $R_L$ = 2.0 $\Omega$ ,
Rise time	tr	—	17	_	ns	$V_{GS}$ = 10 V, $R_G$ = 4.7 $\Omega$
Turn-off delay time	t <sub>d(off)</sub>	—	65	_	ns	
Fall time	t <sub>f</sub>	—	18		ns	
Body-drain diode forward voltage	V <sub>DF</sub>	_	0.94		V	$I_F = 75 \text{ A}, V_{GS} = 0^{\text{Note5}}$
Body-drain diode reverse recovery	t <sub>rr</sub>	_	45	_	ns	$I_F = 75 \text{ A}, V_{GS} = 0,$
time						di <sub>F</sub> /dt = 100 A/µs

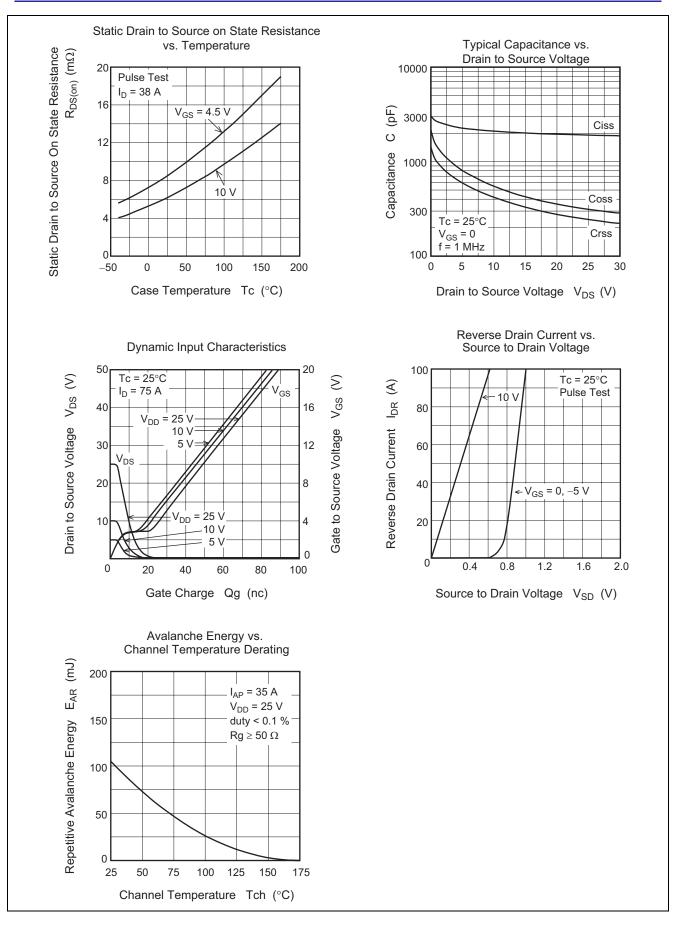
Note: 5. Pulse test



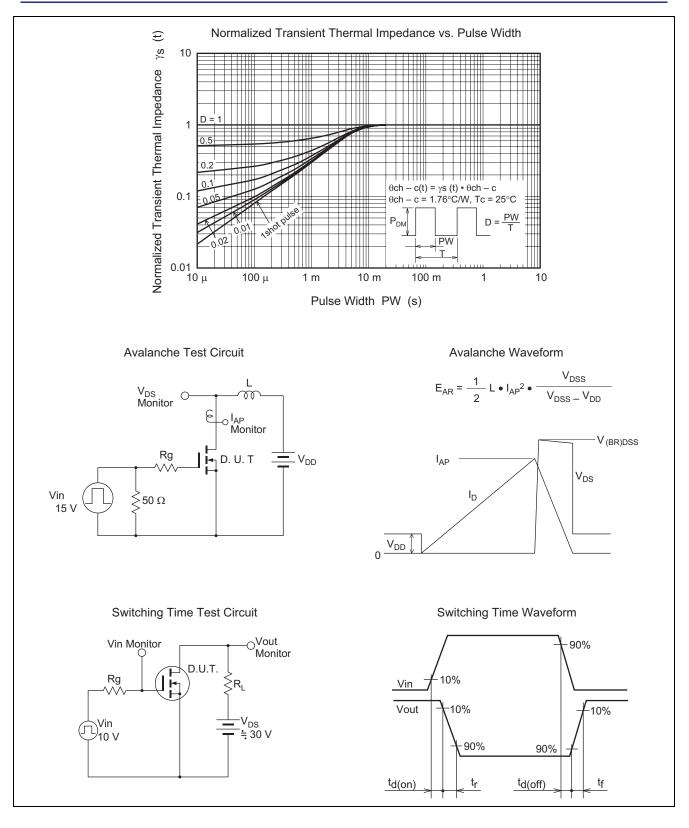
#### **Main Characteristics**





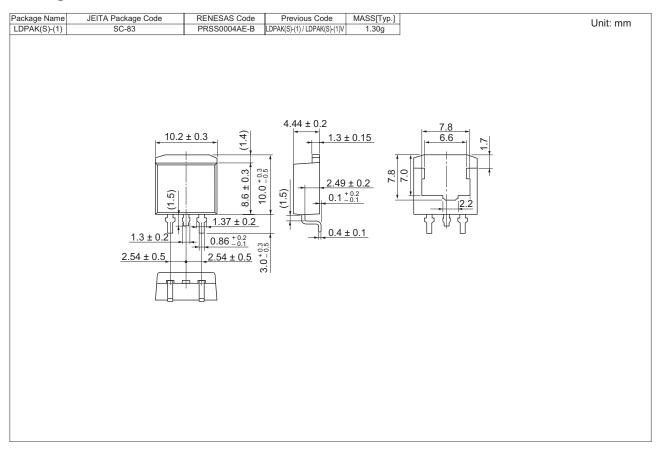








### **Package Dimensions**



## **Ordering Information**

Orderable Part Number	Quantity	Shipping Container
RJK0630JPE-00-J3	1000 pcs	Taping (Sinistrorse)



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